INTERNATIONAL STANDARD ISO/IEC 14496-4:2004
TECHNICAL CORRIGENDUM 1

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Information technology — Coding of audio-visual objects —

Part 4: Conformance testing

TECHNICAL CORRIGENDUM 1

Technologies de l'information — Codage des objets audiovisuels —

Partie 4: Essai de conformité

RECTIFICATIF TECHNIQUE 1

Technical Corrigendum 1 to ISO/IEC 14496-4:2004 was prepared by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, Subcommittee SC 29, *Coding of audio, picture, multimedia and hypermedia information*.

In Table 10, subclause 5.5.7, replace the following:

	SH-7-1	Toshiba	vcon-sh7-1.cmp						S								
	SH-7-2	Toshiba	vcon-sh7-2.cmp						S								
	SH-8-1	Toshiba	vcon-sh8-1.cmp							S							
	SH-8-2	Toshiba	vcon-sh8-2.cmp							S							

with:

	SH-7-1	Toshiba	vcon-sh7-1_reva.cmp						S								
	SH-7-2	Toshiba	vcon-sh7-2_reva.cmp						S								
	SH-8-1	Toshiba	vcon-sh8-1_reva.cmp							S							
	SH-8-2	Toshiba	vcon-sh8-2_reva.cmp							S							

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In Table 10, replace:

Scalability	SCS-1	Sony	vcon-scs1.bits	S	S	S											
	SCS-1_e	Sony	vcon-scs1_e.bits				S	S									
	SCS-2	Sony	vcon-scs2.bits	S	S	S											
	SCS-2_e	Sony	vcon-scs2_e.bits				S	S									
	SCS-3	Sony	vcon-scs3.bits	S	S	S											
	SCS-3_e	Sony	vcon-scs3_e.bits				S	S									
	SCS-8	Sony	vcon-scs8.bits	D													
	SCS-8_e	Sony	vcon-scs8_e.bits				D										
	SCS-9	Sony	vcon-scs9.bits		D												
	SCS-9_e	Sony	vcon-scs9_e.bits					D									

with:

Scalability	SCS-1	Sony	vcon-scs1_reva.bits	S	S	S											
	SCS-1_e	Sony	vcon-scs1_e_reva.bits				S	S									
	SCS-2	Sony	vcon-scs2_reva.bits	S	S	S											
	SCS-2_e	Sony	vcon-scs2_e_reva.bits				S	S									
	SCS-3	Sony	vcon-scs3_reva.bits	S	S	S											
	SCS-3_e	Sony	vcon-scs3_e_reva.bits				S	S									
	SCS-8	Sony	vcon-scs8_reva.bits	D													
	SCS-8_e	Sony	vcon-scs8_e_reva.bits				D										
	SCS-9	Sony	vcon-scs9_reva.bits		D												
	SCS-9_e	Sony	vcon-scs9_e_reva.bits					D									

In Table 10, replace:

Error Resilience	er-1	Toshiba	Vcon-er1.cmp			S											
	er-2-1	Toshiba	Vcon-er2-1.cmp	S													
	er-2-2	Toshiba	Vcon-er2-2.cmp		S												
	er-2-3	Toshiba	Vcon-er2-3.cmp			S											
	er-3-1	Toshiba	Vcon-er3-1.cmp	S													
	er-3-2	Toshiba	Vcon-er3-2.cmp		S												
	er-3-3	Toshiba	Vcon-er3-3.cmp			S											

with:

Error Resilience	er-1	Toshiba	Vcon-er1_reva.cmp			S											
	er-2-1	Toshiba	Vcon-er2-1_reva.cmp	S													
	er-2-2	Toshiba	Vcon-er2-2_reva.cmp		S												
	er-2-3	Toshiba	Vcon-er2-3_reva.cmp			S											
	er-3-1	Toshiba	Vcon-er3-1_reva.cmp	S													
	er-3-2	Toshiba	Vcon-er3-2_reva.cmp		S												
	er-3-3	Toshiba	Vcon-er3-3_reva.cmp			S											

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In Table 12, subclause 5.5.8.1, replace:

mit007.m4v	Simple@L1	Talk	10.000	64	176	144	150	basic

with:

mit007_reva.m4v	Simple@L1	Talk	10.000	64	176	144	150	basic

In Table 14, subclause 5.5.8.1, replace:

Scalable Still Texture	ss-1	Sharp	vcon-ss1.bits								S	S	S		S	S	
	ss-2	Sharp	vcon-ss2.bits								S	S	S		S	S	
	ss-3	Sharp	vcon-ss3.bits								S	S	S		S	S	
	ss-4	Sharp	vcon-ss4.bits								S	S	S		S	S	
	ss-5	Sharp	vcon-ss5.bits								S	S	S		S	S	
	ss-6	Sharp	vcon-ss6.bits								S	S	S		S	S	
	ss-7	Sharp	vcon-ss7.bits								S	S	S		S	S	
	ss-8	Sarnoff	vcon-ss8.bits								S	S	S		S	S	
	ss-9	Sarnoff	vcon-ss9.bits								S	S	S		S	S	
	ss-10	Sarnoff	vcon-ss10.bits								S	S	S		S	S	
	ss-11	Sarnoff	vcon-ss11.bits								S	S	S		S	S	
	ss-12	TI	vcon-ss12.bits								S	S	S		S	S	
	ss-13	TI	vcon-ss13.bits								S	S	S		S	S	

with:

Scalable Still Texture	ss-1	Sharp	v1_vcon-ss1.bits								S	S	S		S	S	
	ss-2	Sharp	v1_vcon-ss2.bits								S	S	S		S	S	
	ss-3	Sharp	v1_vcon-ss3.bits								S	S	S		S	S	
	ss-4	Sharp	v1_vcon-ss4.bits								S	S	S		S	S	
	ss-5	Sharp	v1_vcon-ss5.bits								S	S	S		S	S	
	ss-6	Sharp	v1_vcon-ss6.bits								S	S	S		S	S	
	ss-7	Sharp	v1_vcon-ss7.bits								S	S	S		S	S	
	ss-8	Sarnoff	v1_vcon-ss8.bits								S	S	S		S	S	
	ss-9	Sarnoff	v1_vcon-ss9.bits								S	S	S		S	S	
	ss-10	Sarnoff	v1_vcon-ss10.bits								S	S	S		S	S	
	ss-11	Sarnoff	v1_vcon-ss11.bits								S	S	S		S	S	
	ss-12	TI	v1_vcon-ss12.bits								S	S	S		S	S	
	ss-13	TI	v1_vcon-ss13.bits								S	S	S		S	S	

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In Table 14, replace:

San021.m4v	Simple@L2	Aki1	10.000	128	352	288	99	basic
San022.m4v	Simple@L1	Aki1	10.000	64	176	144	100	VBV(L1)
San023.m4v	Simple@L2	Aki1	10.000	128	352	288	49	VBV(L2)
San024.m4v	Simple@L3	Aki1	10.000	384	352	288	127	VBV(L3)

with:

San021_reva.m4v	Simple@L2	Aki1	10.000	128	352	288	99	basic
San022_reva.m4v	Simple@L1	Aki1	10.000	64	176	144	100	VBV(L1)
San023_reva.m4v	Simple@L2	Aki1	10.000	128	352	288	97	VBV(L2)
San024_reva.m4v	Simple@L3	Aki1	10.000	384	352	288	239	VBV(L3)

In Table 16, subclause 5.5.8.2, replace:

mat000.m4v	Core@L1	own synthetic	66.600	116	16	16	999	IVOP IDCT bitstream1
mat001.m4v	Simple@L1	own synthetic	66.600	30	16	16	999	IVOP IDCT bitstream2

with:

mat000_reva.m4v	Core@L1	own synthetic	66.600	116	16	16	998	IVOP IDCT bitstream1
mat001_reva.m4v	Simple@L1	own synthetic	66.600	30	16	16	998	IVOP IDCT bitstream2

In Table 19, replace:

mat045.m4v	Simple@L1	own synthetic	0.118	64	16	16	2	AC/DC Saturation

with:

mat045_reva.m4v	Simple@L1	own synthetic	0.118	64	16	16	2	AC/DC Saturation

In Table 20, replace:

Pio002.m4v	Core@L1	Friends	10.000	384	176	144	300	Video Packet + Variable Q
Pio003.m4v	Core@L1	Drive	10.000	384	176	144	300	Data partitioning + Variable Q
Pio004.m4v	Core@L1	Octpus	10.000	384	176	144	300	RVLC + Variable Q

with:

Pio002_reva.m4v	Core@L1	friends	10.000	384	176	144	300	Video Packet + Variable Q
Pio003_reva.m4v	Core@L1	drive	10.000	384	176	144	300	Data partitioning + Variable Q
Pio004_reva.m4v	Core@L1	octpus	10.000	384	176	144	300	RVLC + Variable Q

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In Table 22, replace:

Pio005.m4v	Core@L1	aki2	10.000	192	176	144	300	Bianry Shape (Variable Q + intra_dc_vlc_thr)

with:

Pio005.m4v	Core@L1	aki2	10.000	192	176	144	300	Binary Shape (Variable Q + intra_dc_vlc_thr)

In Table 22, replace:

pio006.m4v	Core@L1	bike	10.000	384	176	144	300	Binary Shape (Video Packet + Variable Q)
pio007.m4v	Core@L1	goldfish	10.000	384	176	144	300	Binary Shape (Data partitioning + Variable Q)
pio008.m4v	Core@L1	goldfish	10.000	384	176	144	300	Binary Shape (RVLC + Variable Q)

with:

pio006_reva.m4v	Core@L1	bike	10.000	384	176	144	300	Binary Shape (Video Packet + Variable Q)
pio007_reva.m4v	Core@L1	goldfish	10.000	384	176	144	300	Binary Shape (Data partitioning + Variable Q)
pio008_reva.m4v	Core@L1	goldfish	10.000	384	176	144	300	Binary Shape (RVLC + Variable Q)

In Table 23, replace:

nec008.m4v	Core@L1	drive	10.000	384	176	144	299	Short Header

with:

nec008_reva.m4v	Core@L1	drive	10.000	384	176	144	300	Short Header

In Table 27, subclause 5.6.2, replace:

	GE-3	UH	ge-3_ace_l1.bits													X							
		UH	ge-3_ace_l2.bits														X						
		UH	ge-3_ace_l3.bits															X					
		UH	ge-3_ace_l4.bits																X				

with:

	GE-3	UH	ge3_ace_l1.bits													X							
		UH	ge3_ace_l2.bits														X						
		UH	ge3_ace_l3.bits															X					
		UH	ge3_ace_l4.bits																X				

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In Table 27, replace:

	GE-10	UH	ge-10_ace.bits												S	S	S	S				

with:

	GE-10	UH	ge10_ace.bits												S	S	S	S				

In Table 27, replace:

	GE-12	UH	ge-12_ace_l1.bits												X							
		UH	ge-12_ace_l2.bits													X						
		UH	ge-12_ace_l3.bits														X					
		UH	ge-12_ace_l4.bits															X				

with:

	GE-12	UH	ge12_ace_l1.bits												X							
		UH	ge12_ace_l2.bits													X						
		UH	ge12_ace_l3.bits														X					
		UH	ge12_ace_l4.bits															X				

In Table 27, replace:

	GE-14	UH	ge-14_ace_l1.bits												X							
		UH	ge-14_ace_l2.bits													X						
		UH	ge-14_ace_l3.bits														X					
		UH	ge-14_ace_l4.bits															X				

with:

	GE-14	UH	ge14_ace_l1.bits												X							
		UH	ge14_ace_l2.bits													X						
		UH	ge14_ace_l3.bits														X					
		UH	ge14_ace_l4.bits															X				

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In Table 27, replace:

	GE-23	Bosch	vcon_ge_23_ace_l1.bits													X							
		Bosch	vcon_ge_23_ace_l2.bits														X						
		Bosch	vcon_ge_23_ace_l2.bits															X					
		Bosch	vcon_ge_23_ace_l2.bits																X				

with:

	GE-23	Bosch	vcon_ge_23_ace_l1.bits													X							
		Bosch	vcon_ge_23_ace_l2.bits														X						
		Bosch	vcon_ge_23_ace_l3.bits															X					
		Bosch	vcon_ge_23_ace_l4.bits																X				

In Table 27, replace:

	A1GE-2	UH	a1ge-2_ace_l1.bits													X							
		UH	a1ge-2_ace_l2.bits														X						
		UH	a1ge-2_ace_l3.bits															X					
		UH	a1ge-2_ace_l4.bits																X				

with:

	A1GE-2	UH	a1ge2_ace_l1.bits													X							
		UH	a1ge2_ace_l2.bits														X						
		UH	a1ge2_ace_l3.bits															X					
		UH	a1ge2_ace_l4.bits																X				

In Table 27, replace:

	A1GE-4	UH	a1ge_4_ace.bits													S	S	S	S				

with:

	A1GE-4	UH	a1ge4_ace.bits													S	S	S	S				

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In Table 27, replace:

	A1GE-9	UH	a1ge-9_ace_l1.bits												X							
		UH	a1ge-9_ace_l2.bits													X						
		UH	a1ge-9_ace_l3.bits														X					
		UH	a1ge-9_ace_l4.bits															X				

with:

	A1GE-9	UH	a1ge9_ace_l1.bits												X							
		UH	a1ge9_ace_l2.bits													X						
		UH	a1ge9_ace_l3.bits														X					
		UH	a1ge9_ace_l4.bits															X				

In Table 27, replace:

	A1GE-12	UH	a1ge-12_ace_l1.bits												X							
		UH	a1ge-12_ace_l2.bits													X						
		UH	a1ge-12_ace_l3.bits														X					
		UH	a1ge-12_ace_l4.bits															X				

with:

	A1GE-12	UH	a1ge12_ace_l1.bits												X							
		UH	a1ge12_ace_l2.bits													X						
		UH	a1ge12_ace_l3.bits														X					
		UH	a1ge12_ace_l4.bits															X				

In Table 27, replace:

Binary Shape	SH-1	Siemens	sh1_ace_enc.bits												X							

with:

Binary Shape	SH-1	Siemens	sh1_ace.bits												X							

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Remove the following entries related to missing or invalid conformance bitstreams.

In subclause 5.5.7, remove the following lines from Table 10:

General	GE-1	GI	Vcon-ge1.cmp									S					
	GE-2	GI	Vcon-ge2.cmp									S					
	GE-3	GI	Vcon-ge3.cmp									S					
	GE-4	GI	vcon-ge4.cmp									S					
	GE-6	GI	vcon-ge6.cmp									S					
	GE-8	GI	vcon-ge8.cmp			S											
	GE-10	GI	vcon-ge10.cmp									S					
	GE-11	GI	vcon-ge11.cmp									S					
	GE-12	GI	vcon-ge12.cmp									S					
	GE-14	GI	vcon-ge14.cmp									S					
	GE-18	GI	vcon-ge18.cmp			S											
	GE-19	GI	vcon-ge19.cmp									S					
	GE-20	GI	vcon-ge20.cmp									S					
	GE-21	GI	vcon-ge21.cmp									S					
	GE-22	GI	vcon-ge22.cmp									S					
	GE-23	GI	vcon-ge23.cmp									S					
	GE-24	GI	vcon-ge24.cmp			S											
Binary Shape	SH-1	Sony	Vcon-sh1.bits						S	S	S	S	S				
	SH-2	Sony	Vcon-sh2.bits						S								
	SH-3	Sony	Vcon-sh3.bits							S							
	SH-4	Sony	Vcon-sh4.bits								S						
	SH-5	Sony	Vcon-sh5.bits									S					
	SH-6	Sony	Vcon-sh6.bits										S				
	SH-9-1	Samsung	vcon-sh9-1.cmp								S						
	SH-9-2	Samsung	vcon-sh9-2.cmp								S						
	SH-10-1	Samsung	vcon-sh10-1.cmp									S					
	SH-10-2	Samsung	Vcon-sh10-2.cmp									S					
	MHH-1	Sorenson	Hlfpel1h.bits	X													
	MHH-2	Sorenson	Hlfpel2h.bits	X													
	MHH-3	Sorenson	Hlfpel3h.bits	X													
	MHH-4	Sorenson	Hlfpel4h.bits	X													
	MHH-5	Sorenson	Hlfpel5h.bits	X													
	MHH-6	Sorenson	hlfpel6h.bits	X													
	MHH-7	Sorenson	hlfpel7h.bits	X													
	MVH-1	Sorenson	hlfpel1v.bits	X													
	MVH-2	Sorenson	hlfpel2v.bits	X													
	MVH-3	Sorenson	hlfpel3v.bits	X													
	MVH-4	Sorenson	hlfpel4v.bits	X													
	MVH-5	Sorenson	hlfpel5v.bits	X													
	MVH-6	Sorenson	hlfpel6v.bits	X													
	MVH-7	Sorenson	hlfpel7v.bits	X													
	sp6	Hughes	vcon-sp6.bits									X					

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In subclause 5.6,2, remove the following lines from Table 27:

		Siemens	sh1_ace_enc.bits													X							
		Siemens	sh1_ace_enc.bits														X						
		Siemens	sh1_ace_enc.bits															X					
	SH-2	Sony	Vcon-sh2.bits																S				
	SH-3	Sony	Vcon-sh3.bits																	S			
	SH-4																						
	SH-5																						
	SH-6																						
	SH-9																						
	SH-10																						
	SH-11																						
	A1ST-6	Samsung																	S	S	S	S	S
	A1WT-3	Sharp																	S	S	S	S	S
Error resilience for scalable textures	A1ET-1	Sarnoff																	S	S	S	S	S
	A1ET-2	Sarnoff																	S	S	S	S	S
	A1ET-3	Sarnoff																	S	S	S	S	S
General	A1MHQ-1	Sorenson	qtrpel1h.bits												X								
General	A1MHQ-2	Sorenson	qtrpel2h.bits												X								
General	A1MHQ-3	Sorenson	qtrpel3h.bits												X								
General	A1MHQ-4	Sorenson	qtrpel4h.bits												X								
General	A1MHQ-5	Sorenson	qtrpel5h.bits												X								
General	A1MHQ-6	Sorenson	qtrpel6h.bits												X								
General	A1MHQ-7	Sorenson	qtrpel7v.bits												X								
General	A1MVQ-1	Sorenson	qtrpel1v.bits												X								
General	A1MVQ-2	Sorenson	qtrpel2v.bits												X								
General	A1MVQ-3	Sorenson	qtrpel3v.bits												X								
General	A1MVQ-4	Sorenson	qtrpel4v.bits												X								
General	A1MVQ-5	Sorenson	qtrpel5v.bits												X								
General	A1MVQ-6	Sorenson	qtrpel6v.bits												X								
General	A1MVQ-7	Sorenson	qtrpel7v.bits												X								

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Remove the descriptions of unused bitstreams.

Remove subclauses 5.5.3.1.22 to 5.5.3.1.35 "Test Bitstream #MHH-1", "Test Bitstream #MHH-2", "Test Bitstream #MHH-3", "Test Bitstream #MHH-4", "Test Bitstream #MHH-5", "Test Bitstream #MHH-6", "Test Bitstream #MHH-7", "Test Bitstream #MVH-1", "Test Bitstream #MVH-2", "Test Bitstream #MVH-3", "Test Bitstream #MVH-4", "Test Bitstream #MVH-5", "Test Bitstream #MVH-6", "Test Bitstream #MVH-7".

Remove subclauses 5.5.3.2.2 to 5.5.3.2.6 "Test Bitstream #SH-2", "Test Bitstream #SH-3", "Test Bitstream #SH-4", "Test Bitstream #SH-5", "Test Bitstream #SH-6".

Remove subclause 5.5.3.2.9 "Test Bitstream #SH-9".

Remove subclause 5.5.3.2.10 "Test Bitstream #SH-10".

Remove subclause 5.5.3.5.2 "Test bitstream #er-2".

Remove subclause 5.5.3.5.3 "Test bitstream #er-3".

Remove subclause 5.5.3.7.6 "Test bitstream #sp6".

Remove subclauses 5.6.1.1.17 to 5.6.1.1.30 "Test Bitstream #A1MHQ-1", "Test Bitstream #A1MHQ-2", "Test Bitstream #A1MHQ-3", "Test Bitstream #A1MHQ-4", "Test Bitstream #A1MHQ-5", "Test Bitstream #A1MHQ-6", "Test Bitstream #A1MHQ-7", "Test Bitstream #A1MVQ-1", "Test Bitstream #A1MVQ-2", "Test Bitstream #A1MVQ-3", "Test Bitstream #A1MVQ-4", "Test Bitstream #A1MVQ-5", "Test Bitstream #A1MVQ-6", "Test Bitstream #A1MVQ-7".

Remove subclause 5.6.1.5.6 "Test bitstream #A1ST-6".

Remove subclause 5.6.1.6.3 "Test bitstream #A1WT-3".

Remove subclauses 5.6.1.7.1 to 5.6.1.7.3 "Test bitstream #A1ET-1", "Test bitstream #A1ET-2", "Test bitstream #A1ET-3".

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In the bitstream repository, remove the following bitstreams (invalid bitstreams):

```
Vcon-ge1.cmp
Vcon-ge2.cmp
Vcon-ge3.cmp
Vcon-ge4.cmp
Vcon-ge6.cmp
Vcon-ge8.cmp
Vcon-ge10.cmp
Vcon-ge11.cmp
Vcon-ge12.cmp
Vcon-ge14.cmp
Vcon-ge18.cmp
Vcon-ge19.cmp
Vcon-ge23.cmp
Vcon-ge24.cmp
vcon-sh9-1.cmp
vcon-sh9-2.cmp
vcon-sh10-1.cmp
vcon-sh10-2.cmp
(replaced by the fixed bitstream)
Vcon-er1.cmp
Vcon-er2-1.cmp
Vcon-er2-2.cmp
Vcon-er2-3.cmp
Vcon-er3-1.cmp
Vcon-er3-2.cmp
Vcon-er3-3.cmp
vcon-ss1.bits
vcon-ss2.bits
vcon-ss3.bits
vcon-ss4.bits
vcon-ss5.bits
vcon-ss6.bits
vcon-ss7.bits
vcon-ss8.bits
vcon-ss9.bits
vcon-ss10.bits
vcon-ss11.bits
vcon-ss12.bits
vcon-ss13.bits
San021.m4v
San022.m4v
San023.m4v
San024.m4v
mat000.m4v
mat001.m4v
mat021.m4v
mat045.m4v
Pio002.m4v
Pio003.m4v
Pio004.m4v
Pio006.m4v
Pio007.m4v
Pio008.m4v
Nec008.m4v
mit007.m4v
vcon-sh7-1.cmp
vcon-sh7-2.cmp
vcon-sh8-1.cmp
```


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```
vcon-sh8-2.cmp
vcon-scs1.bits
vcon-scs1_e.bits
vcon-scs2.bits
vcon-scs2_e.bits
vcon-scs3.bits
vcon-scs3_e.bits
vcon-scs8.bits
vcon-scs8_e.bits
vcon-scs9.bits
vcon-scs9_e.bits
 (lack of textual description)
vcon-ss14.bits
vcon-ss15.bits
vcon-ss16.bits
vcon-sp3_LLS_sif.cmp
```

In the bitstream repository, add the following bitstreams (all new bitstreams are attached to this Cor.):

```
v1_vcon-ss1.bits (copied from vcon-ss1.bits)
v1_vcon-ss2.bits (copied from vcon-ss2.bits)
v1_vcon-ss3.bits (copied from vcon-ss3.bits)
v1_vcon-ss4.bits (copied from vcon-ss4.bits)
v1_vcon-ss5.bits (copied from vcon-ss5.bits)
v1_vcon-ss6.bits (copied from vcon-ss6.bits)
v1_vcon-ss7.bits (copied from vcon-ss7.bits)
v1_vcon-ss8.bits (copied from vcon-ss8.bits)
v1_vcon-ss9.bits (copied from vcon-ss9.bits)
v1_vcon-ss10.bits (copied from vcon-ss10.bits)
v1_vcon-ss11.bits (copied from vcon-ss11.bits)
v1_vcon-ss12.bits  (copied from vcon-ss12.bits)
v1_vcon-ss13.bits  (copied from vcon-ss13.bits)
San021_reva.m4v (fixed by SANYO)
San022_reva.m4v (fixed by SANYO)
San023_reva.m4v (fixed by SANYO)
San024_reva.m4v (fixed by SANYO)
mat000_reva.m4v (fixed by MEI/Panasonic)
mat001_reva.m4v (fixed by MEI/Panasonic)
mat021_reva.m4v (fixed by MEI/Panasonic)
mat045_reva.m4v (fixed by NTU)
Pio002_reva.m4v (fixed by Pioneer)
Pio003_reva.m4v (fixed by Pioneer)
Pio004_reva.m4v (fixed by Pioneer)
Pio006_reva.m4v (fixed by Pioneer)
Pio007_reva.m4v (fixed by Pioneer)
Pio008_reva.m4v (fixed by Pioneer)
Nec008_reva.m4v (fixed by NEC)
mit007_reva.m4v (fixed by NTU)
vcon-sh7-1_reva.cmp
vcon-sh7-2_reva.cmp
vcon-sh8-1_reva.cmp
vcon-sh8-2_reva.cmp
vcon-scs1_reva.bits
vcon-scs1_e_reva.bits
vcon-scs2_reva.bits
vcon-scs2_e_reva.bits
vcon-scs3_reva.bits
vcon-scs3_e_reva.bits
```

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vcon-scs8_reva.bits
vcon-scs8_e_reva.bits
vcon-scs9_reva.bits
vcon-scs9_e_reva.bits
Vcon-er1_reva.cmp (fixed by Toshiba)
Vcon-er2-1_reva.cmp(fixed by Toshiba)
Vcon-er2-2_reva.cmp(fixed by Toshiba)
Vcon-er2-3_reva.cmp(fixed by Toshiba)
Vcon-er3-1_reva.cmp(fixed by Toshiba)
Vcon-er3-2_reva.cmp(fixed by Toshiba)
Vcon-er3-3_reva.cmp(fixed by Toshiba)
A1GE-03-L3.cmp (provided by NTT)

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